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MATRIX

Certifies Hazel Infotech's Telecheck

The Integration team at Matrix has tested and certified the Telecheck – telephone monitoring and management software from Hazel Infotech Limited as fully compatible and integrated CAS solution for its ETERNITY range of IP phone systems. As a result of this integration, businesses keen on cost savings will gain additional control over telecom resources. With the use of Telecheck CAS tool with Matrix ETERNITY IP phone systems, organizations can keep track of all the calls placed or received through the phone system.

“Matrix ETERNITY is the advanced IP-PBX offering combination of trunks and users/subscribers for Analog, Digital, IP and GSM networks. ETERNITY IP-PBX is loaded with the range of cost saving features such as least cost routing, GSM network support, call budgeting, automatic call back and many more. Besides, ETERNITY also has buffer memory of 12000 calls for call logs and users can get the print of these records.” said Ruchir Talati, Product Manager – Enterprise Phone Systems for Matrix.

“With this integration, ETERNITY IP phone system users will get additional call controlling and management flexibilities and will be able to implement cost control policies, customize cost allocation, detect toll fraud, verify overbilling, record call events and in turn increase employee productivity through resource optimization.” concluded Ruchir Talati.

“Hazel Infotech is pleased to partner with Matrix in building an enhanced telecheck version optimized to maximize the benefits of ETERNITY IP phone systems. Telecheck is noted for being readily deployable with CAS, and with many additional capabilities, Matrix users can expect to achieve the best performance from their ETERNITY IP phone systems and earn returns many times over on their telecheck investment” said Nikhil Daftary, Vice President – Development at Hazel Infotech.

MATHWORKS

STARC Certifies Matlab and Simulink

MathWorks announced that the Semiconductor Technology Academic Research Center (STARC) in Japan has selected and certified MATLAB, Simulink, and Embedded Coder as the preferred tools for Model-Based Design in its new STARCAD-AMS design flow. STARC is the Japan semiconductor industry consortium of which member companies are Fujitsu semiconductor, Panasonic, Renesas Electronics, Rohm, Sony and Toshiba.

Now, Model-Based Design using MATLAB and Simulink can be incorporated as a pre-qualified system level platform by participant member companies in the standard semiconductor design and verification flows of their Mixed-Signal design project.

The STARCAD-AMS approach standardizes the analog/mixed-signal ASIC design flow and eases migration between system-level design and circuit-level design through a prescribed inter-design tool cooperation methodology. In this improved flow, engineers perform system-level behavioral modeling and simulation in MATLAB and Simulink to take advantage of extensive libraries of blocks and functions, as well as fast simulation speeds. This flow continues with automatic C-code generation with Embedded Coder and custom System Verilog extensions that interface and integrate the behavioral models in industry-standard downstream EDA tools. In an evaluation project with the new design flow – featuring Model-Based Design and automatic code generation, and conducted on a motif circuit prepared by STARC – the development time was reduced by approximately 50 percent.

The STARCAD-AMS design flow uses Model-Based Design to enable three key activities:

- System-level modeling and simulation of behavioral models, which allow engineers to collaborate across system-level and analog-circuit-level design.
- Optimization of system design parameters, by creating an analog/mixed-signal large-scale integration (LSI) design environment at the system level.
- Automatic code generation for interfacing and integration with industry-standard downstream EDA tools, using Embedded Coder.

